Comprehensive Comparison between the Experimental and Simulated Characteristics of the Mono-Crystalline Silicon Solar Cell Using SCAPS-1D

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Keywords silicon solar cell, SCAPS-1D simulation, homogeneity, efficiency.

1 Introduction

In the current times, due to the increasing demand for energy and due to the tremendous development in the field of industry [1] the need has become necessary and urgent to search for new renewable and clean energy sources as an alternative to the use of fossil fuels [2] that are harmful to the environment, especially the ozone layer.

Renewable energy is obtained from natural sources that do not run out when used [3]. Examples of renewable energy are many and varied, including hydroelectric, wind, solar, etc. Solar energy is the third sustainable energy source used to generate energy after wind energy and hydroelectric energy [4] due to the ease of its immediate conversion into energy. Utilization has been expanding for renewable sources due to their abundance in nature, as they are environmentally friendly and non-polluting. It is expected that renewable energy will be used in a large and expanded manner, far exceeding the use of fossil fuels. Photovoltaic cells have an important role in the photovoltaic solar energy system. Solar cells produce electricity through the photoelectric effect, where sunlight generates electricity in certain materials by decomposing their external electrons. Mono-crystalline solar cells have played an effective and significant role in the technology of manufacturing silicon solar cells [5] due to their simplicity and advanced technology [6]. Many researchers in the field of manufacturing photovoltaic cells have conducted many researches and studies that improve the manufacturing process at a low cost, and higher efficiency [7-8]. In solar cells, silicon is the material that is utilized the most frequently. It is also abundantly found in nature as silicon dioxide in quartz and sand, where it is removed using carbon reduction. In actuality, silicon makes up roughly 26% of the earth's crust. Silicon optoelectronic devices are widely used in their manufacture [9]. It was found that one of the best proposed solutions to face the decrease in the traditional energy used is the use of sustainable renewable energy in electricity generation and industry [10], as well as to reduce human impact on theenvironment and increase energy independence [11-12]. Photovoltaic cells convert the enormous solar energy that it reaches the earth to benefit from electrical energy [13].

The efficiency of a solar cell depends on the absorption of photons and the electron-hole pairs produced in the space

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charge region [14, 15].

The main objective of this study is to use scaps-1d to compare the experimental and simulated characteristics of mono-crystalline silicon solar cells. The verification of the simulation results through the experimental ones may assist authors in the future work for conducting other cases studied and providing new designs to improve the performance of solar cell. Both methods' I-Vcharacteristics were compared with the points measured, and the results are indicated good agreement. Mono-crystalline silicon solar cell was used in the Egyptian-Chinese laboratory to conduct the experiment due to its advantages, which are:

• They perform better in low levels of sunlight, making them ideal for cloudy areas. They are perfect in lowlevels of sunlight because they work better in low light.

• They have the highest level of efficiency at 20-25% till 3 - Martial and Methods now.

• They require less space compared to other types due to their high efficiency.

• According to the manufacturers, this type of solarcell has the longest lifespan, most of which have a 25-year warranty.

2 Structure of Solar Cell

Silicon solar cell is an electronic device that converts sunlight directly into electricity [16]. The mono-crystalline cell used in the experimental work consists of an absorber (P-layer) that is textured to form pyramids and a back surface field layer (P+) that is used to repel the minority carriers (electrons) in order to reduce the backside recombination of the absorber, a certain amount of aluminum diffuses into this region, forming a p+ region near the backside, that is, the aluminum backside field. This field pushes electrons from the back to the front, preventing the recombination of minority carriers on the back. [17], emitter (N-layer) that deposits to form the P-N junction, and an antireflection coating layer that is deposited on the front side to reduce the reflected light and front and back electrodes, as shown in Figure 1. Table1. Simulated [p-n] junction parameters for silicon solar cells.



Fig1: Solar cell structure [16]

The study focuses on a solar cell p-n structure based on silicon. Figure 2 shows the simulation process using the (SCAPS-1D) software. There are a lot of options in SCAPS-1D software, for example, the capability to work with amorphous cells (a-Si and micro-morphous Si) and crystalline solar cells (c-Si and GaAs, family) [18].

Launch scaps program	1		
Click set problem			
Input parameters			
Set working condition			
Measure the action			
Run the program			
Fig 2: SCAPS working procedure			

Parameters	P+ BSF	p- bulk	p-si front	n-si
Thickness	5µm	170µm	5 µm	0.5 μ m
Relative permittivity $arepsilon {f r}$	11.68	11.68	11.68	11.68
Electron affinity χe (eV)	8	8	8	8
Band gap energy Eg (eV)	1.12	1.12	1.12	1.12
Electron mobility (cm2.V-1.s-1)	1400	1400	1400	1400
Hole mobility (cm2.V-1.s-1)	460	460	460	460
Effective conduction band	2.8×1019	2.8×1019	2.8×1019	2.8×1019
Density NC(cm-3)				
Effective valence band	1.04×1019	1.04×1019	1.04×1019	1.04×1019
Density NV(cm-3)				
Acceptor concentration	1016	1016	1016	0
NA(cm-3)				
Donor concentration ND(cm-3)	0	0	0	1016

Table 1: Simulated [p-n] junction parameters for silicon solar cells.

The capacity of a photovoltaic cell to transfer the energy of photons that hit it is commonly referred to its conversion efficiency. Now, these measurements are uniform [19]. As a result, the makers test the solar cellsat a constant temperature of 25°C under artificial light that simulates the solar spectrum AM1.5 (the total irradiance received on the Earth's surface at an altitude of 0° with an angle of 48°). At a constant temperature of 25 °C [20]. The PV module's cells got an average total power of 1000W/m² during testing [21]. Researchers are trying to boost the power density of solar cells [22]. The fill factor is a crucial quantity that is frequently used to certify the quality of a PV cell, a cell, or a PV generator from the current-voltage characteristic (FF). This coefficient shows the relationship between the highest powers a cell is capable of producing, P_{max}, and the power that the rectangle-shaped I-V curve can provide [23]. The peak power point of a solar cell can be used to describe it [24]. The largest rectangle gives the maximum power output of a cell, as shown in Figure 3.



Fig. 3: The fill factor.

Fill factor (FF) measures the junction quality [25], asshown in the following equation:

1-FF = ($V_{mp} \times I_{mp}$) / ($V_{oc} \times I_{sc}$)

Fill factor is defined as the ratio of the maximum power (P_{max}) divided by the open circuit voltage (V_{oc}) multiplied by the short circuit current (I_{sc}).

$2-P_{mp} = V_{oc}I_{sc}FF$ Maximum power

It means the maximum power that can produce when working under standard test conditions, as the maximum power is the product of multiplying V_{mp} by I_{mp} .

Short circuit current (Isc):

Is the maximum current that can generate in the case of zero or near zero resistance (under standard test conditions)

Open circuit voltage (Voc):

It is the highest voltage at zero current. The value of Open circuit voltage increases with increased sunlight.

4. Experimental Work

Fabrication of mono-crystalline silicon solar cells deals with a set of chemical components and basic materials [26]. Figure 4 shows the different stages of manufacturing the mono-crystalline solar cell, which takes place in the Egyptian-Chinese factory for the manufacture of solar cells.



Fig. 4: Fabrication process of solar cell.

The coming wafers are P-type substrate doped boron. Wafers characteristics are shown in table 2.

Table 2: Wafer characterization.

Parameters	values	
Area(mm2)	156.75*156.75	
Thickness(µm)	180+20/-10	
Type&dope	P-Boron	
Resistivity(Ω.cm)	0.5-1.5	

4.1. Texturing process

The experiment was carried out at the Egyptian Chinese National Renewable Energy Laboratory. The apparatus used for texturing process is shown in figure 3, it consists of eight tanks (28×28) cm and a depth of (32) cm, were used as shown in the figure.



Fig. 5: Texturing machine.

After finishing the texturing process, the wafers should be dried using the dryer that is shown in figure 6. In the dryer machine, after a good rinsing of the wafers, the drying step will be done in the presence of nitrogen for five minutes at a temperature of 45° C.



Fig. 6: Dryer machine.

4.2. Diffusion process

To deposit the N-layer on the P-type substrate to form the P-N junction, the diffusion furnace is heated gradually (300, then 600, then the required diffusion temperature). After reaching the temperature, wait about 30 minutes for temperature stability shown in Figure 7.



Fig. 7: Boats on the paddle for loading inside the tube.

4.3. Plasma Etching Process

As the phosphorus-doped layer that is deposited during the diffusion process is formed at the edges of the silicon wafer so the cell is short-circuited so the edges must be removed during the plasma etching machine, as shown in Figure 8.



Fig. 8: Plasma etching machine.

4.4. RPSG Process

Removing phosphorus silica glass (RPSG), the wafers are placed in the first tank that contains deionized water and 3.1L of HF for 7 min at room temperature. Then wafers were placed serially in 3 tanks of deionized water at room temperature for 3minforcleaning.

4.5. Plasma-enhanced chemical vapor deposition(PECVD) furnace.

Deposition of the antireflection layer is carried out using silane (SiH4) and ammonia (NH3) and under a vacuum tube (low pressure) for 53 minutes and at the temperature of 380°C.

4.6. Back and front surface metallization (Screen Printing)

Print the impure silver (80% silver & 20% aluminum) on the back side of the cell to form bus bars that are used for welding the cells to each other to form the module. Then the cells are dried in the furnace at 150° C, and Aluminum paste is used in the backside of the cell. Then the cell is dried in the furnace at a temperature of 170° C; after that in the third screen printer, the cells move on a conveyer belt, and the silver paste passes through a screen, as shown in Table 3.



Fig. 9: Screen printer and its furnace.

4.7. The process of testing the solar cell efficiency

After finishing all fabrication steps, the cell should be tested to know its efficiency and electrical parameters sort it for module soldering, minimize any possible mismatch losses in a module, and know the effects of the process parameters variation. I-V Cell Tester is shown in figure 10.

There are some conditions for testing, the testingtemperature is 25° C, the air mass is 1.5, and the incident light power is 1000 W/m².



Fig. 10: I-V Cell tester

Table 3: Wafer shapes after each printing stage.

A comparison of simulated and experimental silicon





Wafer shape after completing the third printing stage

Wafer shape after completing the second printing stage

Wafer shape after completing the first printing stage

solar references cells is shown in table 4; the experimental and simulated values are found to be reasonable

Table 4. Results from simulations and experiments.

Parameters	Experimental results	Simulation values
$\mathbf{V}_{\mathrm{oc}}(\vee)$	0.629	0.627
lsc(A)	8.763	8.729
FF (%)	81.21	83.16
η (%)	18.59	18.72

The homogeneity of the silicon wafer samples is determined using a scanning electron microscope (SEM). The temperature used in the investigation at the Egyptian Chinese Laboratory for new and renewable energy is shown in Figure 11 at 82 degrees Celsius. One found that the pyramids consisted of large sizes with an average grain size (1.8 μ m). The pyramids were less reflective and more homogeneous at 82 degrees Celsius. It was found that the results obtained through the experiment are in great agreement with the results obtained by the researchers Ammar Mahmoud Al-Husseini et al. [27], and Wang et. al. [28].



Fig. 11. SEM image of the silicon wafer surface after etchingat 82° C

5- Results and Discussion

The experimental and simulated values are in decent agreement; it can be concluded that the experimental results are I_{sc} =8.763 A, V_{oc} =0.629 V, FF=81.21%, and η

=18.5%. The Simulation results are I_{sc} = (8.729) A, V_{oc} = 0.627 V, FF= 83. 16 % and η =18.72%. Figure 12. Show (I-V) curve, (P-V) curve of the experimental results of the mono-crystalline silicon solar cell, Figure 13 shows J-V characteristic of the silicon solar cell obtained from simulated results of the mono-crystalline silicon solar cell using scaps-1d.



Figure. 12. Silicon solar cell (I-V) and (P-V) curves.

Fig. 13. A silicon solar cell's J-V characteristic



Current Density

5. CONCLUSION

In conclusion, silica solar cell optimization using numerical simulation using the SCAPS-1D program has been investigated. The study compared silicon solar cells that were simulated and those that were actually made. The installation process is carried out when the degree of achemical reaction inside the basin is 82 ° C, as was discovered through the experiment to obtain homogeneous pyramids formed on the surface of the mono-crystalline solar cell, an appropriate amount of active substance is added, which is 230 ml. Additionally, a suitable quantity of (KOH) is added (0.3357 M0l), and the wafer is allowed to stay in the tank for 14 minutes. a comparison between the experimental and simulated results of the mono-crystalline silicon solar cell using scaps-1d.

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