



WS093:

SoC Verification Platforms Using HW Emulation and Co-modeling Testbench Technologies

By

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Abstract:

High-performance, high-capacity hardware-assisted emulators and co-modeling test bench technology can speed up to 10,000x the verification of any embedded system. We will talk about the general concepts of functional verification using Emulators and show the variety of specialized H/W solutions that can be integrated to the emulator. Also, we will discuss the guidelines for crafting efficient virtual devices and how they can be used to create an SoC verification platform for testing SoC Design Under Tests (DUTs) on the HW emulator

- *Mentor Graphics, Egypt*