



WS094:

Physical Verification landscape; The past & the Present

By

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Abstract:

Complexity in achieving economically profitable yield is tremendously growing with every new technology node. Physical Verification (PV) has already moved from point tool for Design Rules Checking (DRC) to a more complex platform encompasses every verification step after Design and To Silicon implementation. Today, just to name a few, Circuit verification, DFM, LFD, Power,..etc., are all linked to achieve full verification of advanced designs and applications. The past was DRC while the present is Design Verification Solutions that is getting more complex than ever. In this talk, we will present the PV landscape and how Mentor Design To Silicon division is responding to the immense complexity with unprecedented innovation.