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A simple novel method for considering static voltage stability indicator in a power system

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Abstract:

The voltage stability is the ability of the power system to provide adequate reactive power under all operating conditions and to maintain stable load voltage magnitude within specified operating limits. The voltage instability leading to collapse appears to be due to the inability of networks to meet a demand for reactive power at certain critical or weak buses. It can be recognized by noting excessive fall in voltage for small increase in load and increasing difficulty in controlling system voltage. Therefore voltage collapse prediction must take in consideration in power system planning and operation. In this paper a novel indicator from a parallel algorithm will be presented to predict the voltage instability or the proximity of a collapse. The indicator uses the obtained data of a normal load flow to identify the weak buses in the power system. This method has carried out over changing the load power factor. Obtained results for the IEEE 5 bus system considering the effects of STATCOM on voltage stability are presented and discussed.

<u>Keywords:</u>

Voltage Stability, Voltage Instability, Voltage Collapse, Voltage collapse prediction, Voltage Collapse Indicator, FACTS devices and Parallel algorithm.

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1. Introduction:

Voltage instability problem is one of the major problems facing the electric power utilities in many countries. It is also a main concern in power system operation and planning [1]. The problem of voltage instability can be explained as the ability of a power system to maintain steady acceptable voltages at all buses in the system at normal operating conditions and after being subjected to a disturbance [3]. Voltage collapse is the result of a sequence of events that leading to decrease of the system voltage profile which starts gradually and then decreases rapidly suddenly in a major part of the power system [2]. It has been found that voltage magnitudes do not give a good indication of proximity to voltage stability limit [4]. There are a lot of different Indices have been proposed in [5, 6]. The effects of FACTS devices on voltage stability are presented in [7:10]. The objective of this paper is to develop a simple novel indicator from a parallel algorithm by using the obtained data of the load flow problem of large power system for considering voltage stability margin [11:15].

The proposed indicator is based on the Gauss-seidel algorithm which usually used in the solution of load flow problem and studying the effect of FACTS devices on voltage stability margin. This index of the voltage stability, which is derived from the computational techniques for voltage stability assessment, predicts the voltage problem of the system with sufficient accuracy.

2. Fundamentals: Single Generator and Load system:

A simple power system is considered, through which the indicator of the voltage stability is derived. As shown in fig. (1), bus 1 is a generator bus and bus 2 is a load bus whose voltage behavior will be investigated. Test study will be carried out for two cases: (i) Base case, (ii) when installing a STATCOM (capacitor bank) when voltage ≤ 0.95 p.u.

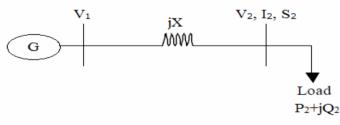


Figure (1): Single generator and Load system

2.1 Base case:

This simple system can be described by the following equations (where the dot above a letter represents a vector):

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$$\dot{I}_{2} = \frac{\dot{V}_{1} - \dot{V}_{2}}{\dot{X}} = \frac{\dot{S}_{2}}{\ddot{V}_{2}}$$
(1)

$${}^{*}_{S_{2}} = \frac{\overset{\bullet}{V_{1}} \overset{*}{V_{2}}}{\overset{\bullet}{X}} - \frac{\overset{\bullet}{V_{2}}}{\overset{\bullet}{X}}$$
(2)

To solve for $|\dot{V}_2|$, equation (2) will be solved, with the assumption that $\dot{S}_2 \dot{X} = a + jb$

Then,

Where: 1, 2 Are the sending end and receiving end angle, respectively.

$$Cos(_{1} - _{2}) = \frac{a + V_{2}^{2}}{V_{1}V_{2}}$$

$$Sin(_{1} - _{2}) = \frac{b}{V_{1}V_{2}}$$
(4)

From equation (4) then,

$$V_1^2 V_2^2 = (a + V_2^2)^2 + b^2 = a^2 + 2aV_2^2 + V_2^4 + b^2$$
(5)

Solve equation (5) to obtain:

$$V_{2} = \sqrt{(V_{1}^{2}/2) - a \pm \sqrt{(V_{1}^{4}/4) - aV_{1}^{2} - b^{2}}}$$
(6)

The obtained voltage has four solutions; only two solutions have physically meaning. These two solutions corresponding to high value and low value of voltage.

The voltage at bus 2 will be collapses, if $(V_1^4/4) - aV_1^2 - b^2 = 0$

When the voltage collapses, it is said that
$$V_2^2 = \frac{V_1^2}{2} - a$$
 (7)

From equation (7), an indicator of the voltage stability will be proposed as:

The Indicator =
$$1 - (2V_2^2 - V_1^2 + 2a) = 1 - (2V_2^2 - V_1^2 + 2Q_2X)$$
 (8)

Then, the derived indicator can be used for predicting the voltage stability problem of the system. The indicator will vary in the range between 0 (no-load of system) and 1 (the voltage at the load bus will collapse).

One example of a single generator and load system was built to determine the proposed indicator, $V_1 = 1.0 \angle 0^\circ$; X = j0.25

For each power factor the load at bus 2 will be changed continuously to find the

collapse point of the system.

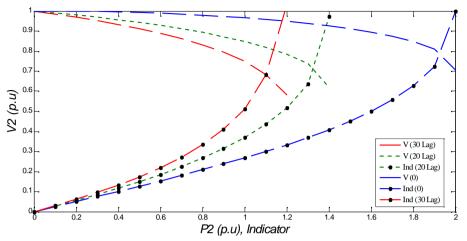


Figure (2): PV Curves and indicators at constant power factor

2.2 When installing a STATCOM:

By installing a STATCOM in bus 2, the system loading will be increased and the system stability margin will be increased as shown in Fig.3.

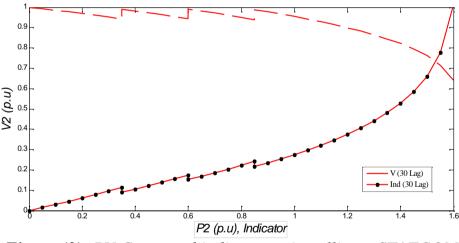


Figure (3): PV Curve and indicator at installing a STATCOM

3.Generalization to an N-bus System:

The results obtained from load flow study are used to deduce the novel indicator using parallel algorithm. The idea of parallel processing method starts with transformation of an n-machine system into n-equivalent single machine to equivalent bus [15]. All the buses can be divided into two categories: Generator bus (PV bus and Slack bus) and Load bus (PQ bus). A single line diagram of the power system is considered to obtain an equivalent model, which is denoted by a single machine to equivalent bus as shown in fig. (4).

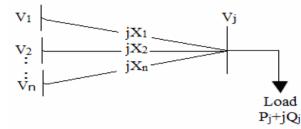


Figure (4): A group of buses are connected to load bus j

The injected current at the load bus will be determined by:

$$\mathbf{I}_{j} = \frac{\mathbf{S}_{j}}{\mathbf{V}_{j}} = \sum_{i=1}^{n} \left(\frac{\mathbf{V}_{i} - \mathbf{V}_{j}}{\mathbf{X}_{i}} \right)$$
(9)

Where : $\overset{\bullet}{V}_{i} = V_{i} e^{j_{i}}$; $\overset{\bullet}{V}_{j} = V e^{j_{j}}$

Rearrange the pervious equation, we obtain:

$$P_{j} = V_{j} \sum_{i=1}^{n} \frac{V_{i}}{X_{i}} Sin(_{i} - _{j})$$

$$Q_{j} = V_{j} \sum_{i=1}^{n} \frac{V_{i}}{X_{i}} Cos(_{i} - _{j}) - V_{j}^{2} \sum_{i=1}^{n} \frac{1}{X_{i}}$$
(10)

From the equivalent bus shown in fig. (5). Another formula for the injected current at load bus can be obtained, as given in equation (11)

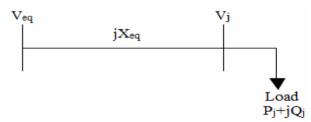


Figure (5): Equivalent bus connected to load bus j



Where : $\overset{\bullet}{V}_{eq} = V_{eq} e^{j_{eq}}$

The following equations will be derived.

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$$P_{j} = \frac{V_{j}V_{eq}}{X_{eq}} Sin(_{eq} - _{j})$$

$$Q_{j} = \frac{V_{j}V_{eq}}{X_{eq}} Cos(_{eq} - _{j}) - \frac{V^{2}}{X_{eq}}$$
(12)

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Comparing the two equations (10) With the two equations (12) Respectively, the equivalent parameters will be obtained.

$$\frac{1}{X_{eq}} = \sum_{i=1}^{n} \frac{1}{X_{i}}$$

$$V_{eq} = X_{eq} \sqrt{(A)^{2} + (B)^{2}}$$

$$_{eq} = Tan^{-1} (\frac{A}{B})$$
(13)

Where:
$$A = \sum_{i=1}^{n} \frac{V_i}{X_i} Sin_i$$
; $B = \sum_{i=1}^{n} \frac{V_i}{X_i} Cos_i$

From equations (12).

$$Sin(_{eq} - _{j}) = \frac{P_{j}X_{eq}}{V_{j}V_{eq}}$$

$$Cos(_{eq} - _{j}) = \frac{Q_{j}X_{eq} + V_{j}^{2}}{V_{j}V_{eq}}$$
(14)

From equations (14).

$$V_{j}^{4} + bV_{j}^{2} + c = 0$$
(15)
Where : $b = 2Q_{j}X_{eq} - V_{eq}^{2}$; $c = X_{eq}^{2}(P^{2} + Q^{2})$

Solve equation (15), the voltage will have four solutions, two of them have physical meaning. These two solutions will correspond to the high and the low value of voltage. The voltage at bus j will collapse, if $b^2 - 4c = 0$

i.e.,
$$V_j^2 = \frac{-b}{2}$$
 (16)

From equation (16), the proposed indicator of the voltage stability of the load bus j will be easily obtained as :

Indicator_j = 1 -
$$(2V_j^2 + b) = 1 - (2V_j^2 + 2Q_jX_{eq} - V_{eq}^2)$$
 (17)

The indicator for the power system can be obtained by:

$$Indicator_{system} = \underset{j \in L}{Max} (Indicator_{j})$$

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Then an indicator, which can be used for predicting the voltage stability problem of the power system, has been derived from parallel processing. Thereby, it is clear that the indicator of the voltage stability at any load bus mainly influenced by two parts: the load at bus j itself, and the 'contributions' of the other load buses as shown at equation (13). If the load at a load bus is changed, the indicator will be influenced; therefore the voltage stability problem is a system-wide problem and not a local one.

3.1 Case study:

The proposed approach is adapted on IEEE 5-bus, with bus1 is taken as Slack bus. The system is shown in fig. (6).

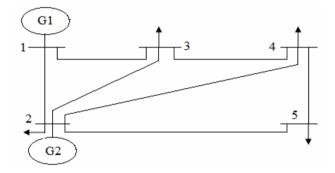


Figure (6): IEEE 5-bus system

<u>3.1.1 Case one:</u>

When the load increase at all buses. The indicator will provide that bus 5 will be collapsed. The voltage stability margin based on the computed indicator as shown in fig. (7).

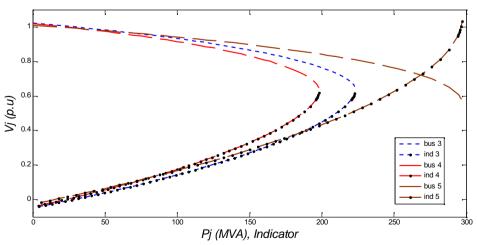


Figure (7): IEEE 5-bus system (case one) all loads increase

3.1.2 Case two:

When the load at bus 5 increased with a constant power factor. The indicator is continuously increasing as the load increases as shown in fig. (8).

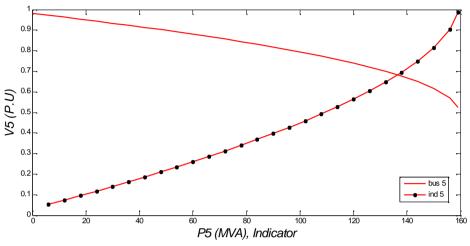


Figure (8): IEEE 5-bus system (case two) loads at bus 5 increase

3.1.3 Case three:

The load at bus 5 will increase results a decrease in the voltage level at all buses. The indicator will provide that bus 5 is collapsed and by installing a STATCOM the voltage stability will be improved and the system loading will be increased. The system stability margin is shown in fig. (9).

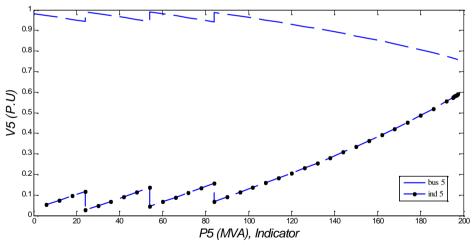


Figure (9): IEEE 5-bus system (case three) load at bus 5 increases, installing a STATCOM

4. Conclusions:

A novel indicator from a parallel algorithm for considering voltage stability of the power system is presented. The indicator can predict the voltage stability problem correctly and properly by using steady-state data. Through the indicator, it is very easy to determine the weak buses of the system and the collapse point of the system can easily predicted. The using of STATCOM will improve the voltage stability and the system stability margin.

<u>References:</u>

- [1] P. A. Lof, G. Anderson, and D. J. Hill, "Voltage Stability Indices For Stressed Power System," IEEE Trans. on Power Systems, vol. 8, pp. 326-335, Feb. 1993.
- [2] Kessel P, Glavitsch H. "*Estimating the voltage stability of a power system*", IEEE Trans Power Delivery 1986; PWRD-1(3):346–54.
- [3] Nan Cutsem T. and Vournas C., "Voltage Stability of Electric Power Systems", Kluwer academic publishers, Boston, USA, 1998, 378 p.
- [4] Clark HK. New challenges: *voltage stability*, IEEE Power ENG. Rev 1990; April:33–7
- [5] J.H.CHOW, F.F.WU, J.A.MOMOH, "Applied Mathematics for Restructured Electric Power Systems Optimization, Control, and Computational Intelligence", Springer US, ISBN 978-0-387-23471-7 (Online), 2005, pp. 25-37.
- [6] H.S.Xiaoxin, Z.R.Li, "Accuracy Analysis of Static Voltage Stability Indices Based On Power Flow Model", Transmission and Distribution Conference and Exhibition: Asia and Pacific, 2005 IEEE/PES, 2005, pp.1-7.
- [7] X.P.Zhang, C.Rehtanz, B.Pal, "Flexible AC Transmission Systems: Modeling and Control", Springer, Berlin, 2006, pp. 189-216.
- [8] R. Natesan, G. Radman, "Effects of STATCOM, SSSC and UPFC on voltage stability", Proc. of the Thirty- Sixth Southeastern Symposium, 2004, pp.546 550.
- [9] J.M.Ramirez, J.L.Murillo-Perez, "*Steady-state voltage stability with Stat COM*", IEEE Trans. On Power Systems, Vol. 21, Issue 3, Aug., 2006, pp. 1453 1454.
- [10] A.R. Messina, M.A. Pe'rez, E. Herna'ndez, "Coordinated application of FACTS devices to enhance steady-state voltage stability", International Journal of Electrical Power & Energy Systems, Volume 44, Issue 6, 2003, pp. 259–267.
- [11] Hariri s. and Parashar M., "Tools and Environments for Parallel and Distributed Computing", Wiley- IEEE, 2004
- [12] Koester D. P., Ranka S., and Fox G. C., "A Parallel Gauss-Seidel Algorithm for Sparse Power System Matrices", in Proceedings of Supercomputing, pp. 184-193, 1994.

- [13] "PVM: Parallel Virtual Machine," available at: http://www.csm.ornl.gov/pvm/.
- [14] Wikipedia, "*Parallel Virtual Machine*", The free Encyclopedia, available at: http://en.wikipedia.org/wiki/pvm.
- [15] M. A. Ali and W. M. Mansour: "A Proposed Approach for Online Transient Stability of Multimachine Power Systems Using Parallel Microprocessors", ICEENG 2008 proceedings, EE149, pp: 901-910.

Nomenclatures:

- δ_1, δ_2 ... Voltage angle of sending end and receiving end respectively
 - α_{eq} ... Voltage angle of the equivalent bus
 - I_{i} ... Injected current at load bus-j
 - P_i ... Injected active power at bus-j
 - Q_j ... Injected reactive power at bus-j
 - S_i ... Injected apparent power at bus-j
- V_i, V_j, \dots Voltage of bus-i, and bus-j respectively
 - Voltage of equivalent bus